We Claim:

1. A device for controlling one or more memory modules, comprising:

a first memory module with a temperature sensor for detecting the temperature of the first memory module, the temperature sensor being arranged in the first memory module;

a second memory module with a second temperature sensor for detecting the temperature of the second memory module, the second temperature sensor being arranged in the second memory module;

a means for determining a highest temperature; and

a memory control module, the memory control module being connected to the first and second memory modules via the means for determining the highest temperature, the memory control module being designed such that an adaptation operation is initiated, if the highest temperature exceeds a predetermined value.

2. The device as claimed in claim 1, wherein the first and second memory modules having pulse width coders, the pulse width coders generating pulse-width-coded temperature signals, the signals being connected upstream of the means for determining the highest temperature,

the means for determining the highest temperature having a wired OR circuit, the circuit combining the pulse-width-coded temperature signals.

3. A method for controlling one or more memory modules, comprising:

transmitting temperature signals from a first and second memory module to the means for determining a highest temperature;

determining the highest temperature;

communicating the temperature signal corresponding to the highest temperature to a memory control module;

evaluating the temperature signal corresponding to the highest temperature; and

initiating an adaptation operation, if the temperature of the highest memory module exceeds a predetermined value.

4. The method as claimed in claim 3, wherein a number of commands per unit time transmitted to the first and second memory modules is reduced by the adaptation operation.

5. The method as claimed in claim 3, wherein the temperature in the first and second memory modules is lowered by the adaptation operation activating a cooling unit is activated.

6. The method as claimed in claim 3, wherein a number of memory refreshes per unit time is increased by the adaptation operation.

7. The method as claimed in claim 3, wherein one of the memory modules is deactivated in a predetermined manner by the adaptation operation.

8. The method as claimed in claim 3, wherein a system is ramped down in a predetermined manner by the adaptation operation.

9. The method as claimed in claim 3, wherein the temperature is binary-coded.

10. The method as claimed in claim 3, wherein the temperature is converted into a frequency-coded temperature signal.

11. The method as claimed in claim 3, wherein the temperature is converted into a pulse-width-coded temperature signal.

12. The method as claimed in claim 3, wherein the temperature is converted into an analog temperature signal.

13. A device for controlling one or more memory modules, comprising:

a first memory module with a temperature sensor for detecting the temperature of the first memory module, the temperature sensor being arranged in the first memory module;

a second memory module with a second temperature sensor for detecting the temperature of the second memory module, the second temperature sensor being arranged in the second memory module;

a measurer for determining a highest temperature; and

a memory control module, the memory control module being connected to the first and second memory modules via the measurer, the memory control module being designed such that an adaptation operation is initiated, if the highest temperature exceeds a predetermined value.

14. The device as claimed in claim 13, wherein the first and second memory modules having pulse width coders, the pulse width coders generating pulse-width-coded temperature signals, the signals being connected upstream of the measurer, the measurer having a wired OR circuit, the circuit combining the pulse-width-coded temperature signals.

15. A method for controlling one or more memory modules, comprising:

transmitting temperature signals from a first and second memory module to the measurer for determining a highest temperature;

determining the highest temperature;

communicating the temperature signal corresponding to the highest temperature to a memory control module;

evaluating the temperature signal corresponding to the highest temperature; and

initiating an adaptation operation, if the temperature of the highest memory module exceeds a predetermined value.

16. The method as claimed in claim 15, wherein a number of commands per unit time transmitted to the first and second memory modules is reduced by the adaptation operation.

17. The method as claimed in claim 15, wherein the temperature in the first and second memory modules is lowered by the adaptation operation activating a cooling unit is activated.

18. The method as claimed in claim 15, wherein a number of memory refreshes per unit time is increased by the adaptation operation.

19. The method as claimed in claim 15, wherein one of the memory modules is deactivated in a predetermined manner by the adaptation operation.

20. The method as claimed in claim 15, wherein a system is ramped down in a predetermined manner by the adaptation operation.

21. The method as claimed in claim 15, wherein the temperature is binary-coded.

22. The method as claimed in claim 15, wherein the temperature is converted into a frequency-coded temperature signal.

23. The method as claimed in claim 15, wherein the temperature is converted into a pulse-width-coded temperature signal.

24. The method as claimed in claim 15, wherein the temperature is converted into an analog temperature signal.